

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Morgan

Art Unit: 2674

Serial No.: 09/088,674

Examiner: Nguyen, Kevin M.

Filed: 2 June 1998

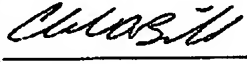
Docket No. TI-25995

For: BOUNDARY DISPERSION FOR MITIGATING PWM TEMPORAL CONTOURING
ARTIFACTS IN DIGITAL DISPLAYS

**RECEIVED
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6 September 2005

Commissioner for Patents
P.O. Box 1450
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MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8 (a)	
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia, 22313-1450, or facsimile transmitted to the U.S. Patent and Trademark Office, on the date shown below.	
	6 Sept. 2005
Charles A. Brill	Date

Dear Sir:

In response to the Examiner's Action mailed 5 April 2005, applicant responds as follows: